

UNITED STATES PATENT APPLICATION FOR:

ADHESION IMPROVEMENT FOR LOW K DIELECTRICS

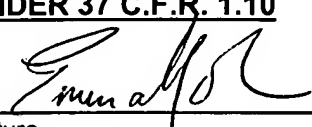
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ADHESION IMPROVEMENT FOR LOW K DIELECTRICS

BACKGROUND OF THE DISCLOSURE

Field of the Invention

[0001] The invention relates to the fabrication of integrated circuits and to a process for depositing dielectric layers on a substrate and the structures formed by the dielectric layer.

Description of the Related Art

[0002] One of the primary steps in the fabrication of modern semiconductor devices is the formation of metal and dielectric layers on a substrate by chemical reaction of gases. Such deposition processes are referred to as chemical vapor deposition or CVD. Conventional thermal CVD processes supply reactive gases to the substrate surface where heat-induced chemical reactions take place to produce a desired layer.

[0003] Semiconductor device geometries have dramatically decreased in size since such devices were first introduced several decades ago. Since then, integrated circuits have generally followed the two year/half-size rule (often called Moore's Law), which means that the number of devices that will fit on a chip doubles every two years. Today's fabrication plants are routinely producing devices having 0.35 μ m and even 0.18 μ m feature sizes, and tomorrow's plants soon will be producing devices having even smaller geometries.

[0004] To further reduce the size of devices on integrated circuits, it has become necessary to use conductive materials having low resistivity and to use insulators having low dielectric constants (dielectric constant < 4.0) to also reduce the capacitive coupling between adjacent metal lines. One such low k dielectric material is spin-on glass, such as un-doped silicon glass (USG) or fluorine-doped silicon glass (FSG), which can be deposited as a gap fill layer in a semiconductor manufacturing process. Another low k dielectric material is silicon oxycarbide that can be used as a dielectric layer in fabricating damascene features.

[0005] One conductive material gaining acceptance is copper and its alloys, which have become the materials of choice for sub-quarter-micron interconnect technology because copper has a lower resistivity than aluminum, ($1.7 \mu\Omega\text{-cm}$ compared to $3.1 \mu\Omega\text{-cm}$ for aluminum), a higher current and higher carrying capacity. These characteristics are important for supporting the higher current densities experienced at high levels of integration and increased device speed. Further, copper has good thermal conductivity and is available in a very pure state.

[0006] One difficulty in using copper in semiconductor devices is that copper is difficult to etch to achieve a precise pattern. Etching with copper using traditional deposition/etch processes for forming interconnects has been less than satisfactory. Therefore, new methods of manufacturing interconnects having copper containing materials and low k dielectric materials are being developed.

[0007] One method for forming vertical and horizontal interconnects is by a damascene or dual damascene method. In the damascene method, one or more dielectric materials, such as the low k dielectric materials, are deposited and pattern etched to form the vertical interconnects, i.e. vias, and horizontal interconnects, i.e., lines. Conductive materials, such as copper containing materials, and other materials, such as barrier layer materials used to prevent diffusion of copper containing materials into the surrounding low k dielectric, are then inlaid into the etched pattern. Any excess copper containing materials and excess barrier layer material external to the etched pattern, such as on the field of the substrate, are then removed.

[0008] However, when silicon oxycarbide layers and silicon carbide layers are used as the low k material in damascene formation, less than satisfactory interlayer adhesion has been observed during processing. Some techniques for processing substrates may apply forces that can increase layering defects, such as layer delamination. For example, excess copper containing materials may be removed by mechanical abrasion between a substrate and a polishing pad in a chemical mechanical polishing process, and the force between the substrate and the polishing pad may induce stresses in the deposited low k dielectric materials to

result in layer delamination. In another example, annealing of deposited materials may induce high thermal stresses that can also lead to delamination in low k dielectric materials.

[0009] Therefore, there remains a need for a process for improving interlayer adhesion between low k dielectric layers.

SUMMARY OF THE INVENTION

[0010] Aspects of the invention generally provide a method for depositing an adhesion layer having a low dielectric constant between two low k dielectric layers. In one aspect, the invention provides a method for processing a substrate including positioning the substrate in a processing chamber, wherein the substrate has a barrier layer comprising at least silicon and carbon, introducing an organosilicon compound and an oxidizing gas at a first ratio of organosilicon compound to oxidizing gas into the processing chamber, generating a plasma of the oxidizing gas and the organosilicon compound to form an initiation layer on the barrier layer, introducing the organosilicon compound and the oxidizing gas at a second ratio of organosilicon compound to oxidizing gas greater than the first ratio into the processing chamber, and depositing a first dielectric layer adjacent the dielectric initiation layer, wherein the dielectric layer comprises silicon, oxygen, and carbon and has a dielectric constant of about 3 or less.

[0011] In another aspect of the invention, a method is provided for processing a substrate including positioning the substrate in a processing chamber, wherein the substrate has a barrier layer comprising silicon, nitrogen, and carbon, introducing an inert gas into the processing chamber, generating a first plasma from a single-frequency RF power source to modify a surface of the barrier layer, introducing an organosilicon compound and an oxidizing gas in a ratio of about 1:1 into the processing chamber, generating a second plasma from a dual-frequency RF power source to form an initiation layer on the barrier layer, introducing the organosilicon compound and the oxidizing gas in a ratio of greater than about 10:1 into the processing chamber, and depositing a first dielectric layer adjacent the dielectric

initiation layer, wherein the dielectric layer comprises silicon, oxygen, and carbon and has a dielectric constant of about 3 or less.

[0012] In another aspect of the invention, a method is provided for processing a substrate including positioning the substrate in a processing chamber, wherein the substrate has a barrier layer comprising at least silicon and carbon, introducing an oxidizing gas into the processing chamber, generating a plasma of the oxidizing gas and treating a surface of the barrier layer, introducing an organosilicon compound at a first flow rate, depositing an initiation layer on the barrier layer from the oxidizing gas and the organosilicon compound, introducing the organosilicon compound at a second flow rate greater than the first flow rate, depositing a first dielectric layer adjacent the dielectric initiation layer from the oxidizing gas and the organosilicon compound, wherein the dielectric layer comprises silicon, oxygen, and carbon and has a dielectric constant of about 3 or less.

[0013] In another aspect of the invention, a method is provided for processing a substrate including positioning the substrate in a processing chamber, wherein the substrate has a barrier layer comprising at least silicon and carbon, introducing an oxidizing gas into the processing chamber, generating a plasma of the oxidizing gas and forming an initiation layer on the barrier layer, introducing an organosilicon compound into the processing chamber, reacting the organosilicon compound and the oxidizing gas, and depositing a first dielectric layer adjacent the initiation layer, wherein the dielectric layer comprises silicon, oxygen, and carbon and has a dielectric constant of about 3 or less.

BRIEF DESCRIPTION OF THE DRAWINGS

[0014] So that the manner in which the above aspects of the invention are attained and can be understood in detail, a more particular description of the invention, briefly summarized above, may be had by reference to the embodiments thereof which are illustrated in the appended drawings.

[0015] It is to be noted, however, that the appended drawings illustrate only typical embodiments of this invention and are therefore not to be considered limiting of its scope, for the invention may admit to other equally effective embodiments.

[0016] Figure 1 is a cross-sectional view showing a dual damascene structure comprising the silicon carbide and silicon oxycarbide layers described herein; and

[0017] Figures 2A-2H are cross-sectional views showing one embodiment of a dual damascene deposition sequence of the invention.

[0018] For a further understanding of aspect of the invention, reference should be made to the ensuing detailed description.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0019] Aspects of the invention described herein refer to a method and apparatus for depositing an adhesive dielectric material and/or treating the surface between dielectric layers to improve interlayer adhesion of dielectric layers. Improving interlayer adhesion may comprise forming a dielectric initiation layer before depositing a subsequent dielectric layer. The initiation layer may comprise silicon, carbon, and optionally, oxygen. Treatments to improve adhesion between the dielectric layers include modifying the surface of a deposited layer prior to subsequent deposition, for example, the application of a plasma treatment of an inert gas, an oxidizing gas, or both. Treating of the surface of a silicon, carbon, and optionally, oxygen containing material is believed to form a more oxide-like surface on the deposited material to thereby improve interlayer adhesion.

Deposition of a Dual Damascene Structure

[0020] A damascene structure that is formed using the deposition processes described herein for a silicon oxycarbide layer disposed on a silicon carbide layer is shown in Figure 1. The following structure forming process as described in Figures 1 and 2A-2H are illustrative and should not be construed or interpreted as limiting the scope of the invention. While the following interlayer adhesion processes are used between a silicon carbide barrier layer 112 and a dielectric layer 110 as well as a low k etch stop layer 114 and an interlayer dielectric layer 118, the invention

contemplates that the interlayer adhesion processes may be used between any suitable dielectric layers in a damascene structure or dielectric stack.

[0021] A substrate 100 having metal features 107 formed in a substrate surface material 105 therein is provided to a processing chamber. A first silicon carbide barrier layer 112 is generally deposited on the substrate surface to eliminate inter-level diffusion between the substrate and subsequently deposited material. The first silicon carbide barrier layer 112 may be nitrogen and/or oxygen doped. Barrier layer materials may have dielectric constants of up to about 9, such as 4 or less, and preferably between about 2.5 and less than about 4. Silicon carbide barrier layers may have dielectric constants of about 5 or less, preferably less than about 4. A capping layer of nitrogen free silicon carbide (not shown) may be deposited in situ on the first silicon carbide barrier layer 112 by minimizing or eliminating the nitrogen source gas. An initiation layer 113 may be deposited on the first silicon carbide barrier layer 112, and a pre-treatment process as described herein may be used prior to depositing the initiation layer 113.

[0022] The first dielectric layer 110 of the oxidized organosilicon compound is deposited on initiation layer 113. The first dielectric layer 110 may then be post-treated with a plasma or e-beam process. Alternatively, a silicon oxide cap layer (not shown) may be deposited in situ on the first dielectric layer 110 by increasing the oxygen concentration in the silicon oxycarbide deposition process described herein to remove carbon from the deposited material.

[0023] An etch stop (or second barrier layer) 114 of a silicon carbide, which may be doped with nitrogen or oxygen, is then deposited on the first dielectric layer 110. The etch stop 114 may have a nitrogen free silicon carbide capping layer deposited thereon. The etch stop 114 is then pattern etched to define the openings of the contacts/vias 116. An interlayer adhesion layer or initiation layer 115 as described herein may be formed on the layer 114 prior to subsequent processing, such as etching or additional dielectric etching, to improve interlayer adhesion with subsequently deposited dielectric materials. The improved adhesion layer may comprise the pre-treatment process and initiation layer as described herein. The

interlayer adhesion surface may be formed by the techniques described herein. A second dielectric layer 118 of an oxidized organosilane or organosiloxane is then deposited over the patterned etch stop. The second dielectric layer 118 may then be plasma or e-beam treated and/or have a silicon oxide cap material disposed thereon by the process described herein.

[0024] A resist 122, conventionally known in the art, such as photoresist material UV-5, commercially available from Shipley Company Inc., of Marlborough, Massachusetts, is then deposited and patterned by conventional means known in the art to define the interconnect lines 120. A single etch process is then performed to define the interconnect down to the etch stop and to etch the unprotected dielectric exposed by the patterned etch stop to define the contacts/vias.

[0025] A preferred dual damascene structure fabricated in accordance with the invention includes the plasma treatment or e-beam treatment of an exposed silicon oxycarbide layer as shown in Figure 2E, and the method of making the structure is sequentially depicted schematically in Figures 2A-2H, which are cross-sectional views of a substrate having the steps of the invention formed thereon.

[0026] As shown in Figure 2A, a first silicon carbide barrier layer 112 is deposited on the substrate surface. The silicon carbide material of the first silicon carbide barrier layer 112 may be doped with nitrogen and/or oxygen. While not shown, a capping layer of nitrogen free silicon carbide or silicon oxide may be deposited on the barrier layer 112. The nitrogen free silicon carbide or silicon oxide may be deposited in situ by adjusting the composition of the processing gas.

[0027] The initiation layer 113 may be deposited by a plasma treatment of the first silicon carbide barrier layer 112 followed by the actual initiation layer material deposition; both processes may be performed sequentially in situ. Helium (He), argon (Ar), neon (Ne), and combinations thereof, may be used for the plasma treatment.

[0028] An example of the inert gas pre-treatment process comprises providing helium to a processing chamber at a flow rate of about 1500 sccm, maintaining a

chamber pressure of about 5 Torr, maintaining a substrate temperature of about 350°C, positioning a gas distributor at about 450 mils from the substrate surface, and generating a plasma by applying a RF power level of about 300 W at a high frequency of about 13.56MHz for a period of 15 seconds.

[0029] An example of a deposition of initiation layer 113 comprises introducing oxygen at a flow rate of 500 sccm into the processing chamber, introducing octamethylcyclotetrasiloxane at a flow rate of about 500 milligrams/minute (mgm) (which corresponds to about 39 sccm for OMCTS), introducing helium at a flow rate of about 4800 sccm, maintaining the chamber at a substrate temperature of about 350°C, maintaining a chamber pressure of about 5 Torr, positioning a gas distributor at about 350 mils from the substrate surface, and applying a RF power of about 500 watts at 13.56 Mhz and about 150 watts at 356 KHz.

[0030] An initial first dielectric layer 110 of silicon oxycarbide from an oxidized organosilane or organosiloxane by the process described herein, such as trimethylsilane and/or octamethylcyclotetrasiloxane, is deposited on initiation layer 113 to a thickness of about 5,000 to about 15,000 Å, depending on the size of the structure to be fabricated. The first dielectric layer may also comprise other low k dielectric material such as a low polymer material including paralyne or a low k spin-on glass such as un-doped silicon glass (USG) or fluorine-doped silicon glass (FSG). The first dielectric layer may then be treated by a plasma process as described herein.

[0031] As shown in Figure 2B, the low k etch stop 114, which may be nitrogen and/or oxygen doped silicon carbide, is then deposited on the first dielectric layer to a thickness of about 100 Å to about 1000 Å. An interlayer dielectric adhesion layer or surface 115 formed by one of the processes described herein, such as a dielectric initiation layer, is then formed or deposited on the low k etch stop layer 114. The low k etch stop 114 and/or interlayer dielectric adhesion layer or surface 115 may be plasma treated as described herein for the silicon carbide materials or

silicon oxycarbide materials. Layer 115 may be deposited as described for initiation layer 113.

[0032] The low k etch stop 114 is then pattern etched to define the contact/via openings 116 and to expose first dielectric layer 110 in the areas where the contacts/vias are to be formed as shown in Figure 2C. Preferably, the low k etch stop 114 is pattern etched using conventional photolithography and etch processes using fluorine, carbon, and oxygen ions. While not shown, a nitrogen-free silicon carbide or silicon oxide cap layer between about 100 Å to about 500 Å may be deposited on the low k etch stop 114 and/or interlayer dielectric adhesion layer or surface 115 prior to depositing further materials.

[0033] After low k etch stop 114 has been etched to pattern the contacts/vias and the resist material has been removed, a second dielectric layer 118 of silicon oxycarbide from an oxidized organosilane or organosiloxane by the process described herein, such as trimethylsilane, is deposited to a thickness of about 5,000 to about 15,000 Å as shown in Figure 2D. The second dielectric layer 118 may then be treated by a plasma process as described herein for first dielectric layer 110.

[0034] A resist material 122 is then deposited on the second dielectric layer 118 (or cap layer) and patterned preferably using conventional photolithography processes to define the interconnect lines 120 as shown in Figure 2E. The resist material 122 comprises a material conventionally known in the art, preferably a high activation energy resist material, such as UV-5, commercially available from Shipley Company Inc., of Marlborough, Massachusetts. The interconnects and contacts/vias are then etched using reactive ion etching or other anisotropic etching techniques to define the metallization structure (*i.e.*, the interconnect and contact/via) as shown in Figure 2F. Any resist material or other material used to pattern the etch stop 114 or the second dielectric layer 118 is removed using an oxygen strip or other suitable process.

[0035] The metallization structure is then formed with a conductive material such as aluminum, copper, tungsten or combinations thereof. Presently, the trend is to

use copper to form the smaller features due to the low resistivity of copper (1.7 m Ω -cm compared to 3.1 m Ω -cm for aluminum). Preferably, as shown in Figure 2G, a suitable metal barrier layer 124, such as tantalum nitride, is first deposited conformally in the metallization pattern to prevent copper migration into the surrounding silicon and/or dielectric material. Thereafter, copper 126 is deposited using either chemical vapor deposition, physical vapor deposition, electroplating, or combinations thereof to form the conductive structure. Once the structure has been filled with copper or other conductive metal, the surface is planarized using chemical mechanical polishing, as shown in Figure 2H.

Initiation Layer Deposition

[0036] In one aspect, interlayer adhesion may be improved by depositing an initiation layer prior to depositing the silicon oxycarbide layer. Optionally, a pre-treatment process of the underlying dielectric layer, such as silicon carbide or doped silicon carbide, may be performed prior to deposition of the initiation layer. The application of the RF power to generate a plasma may not end during gas transition between pre-treatment steps and/or deposition steps.

[0037] The following deposition processes are described with use of the 300 mm Producer™ dual deposition station processing chamber, and should be interpreted accordingly, for example, flow rates are total flow rates and should be divided by two to describe the process flow rates at each deposition station in the chamber. Additionally, it should be noted that the respective parameters may be modified to perform the plasma processes in various chambers and for different substrate sizes, such as for 200mm substrates.

[0038] The deposition processes described herein may be performed in one continuous plasma process, or comprise two or more generated plasma, for example, one for each layer deposition step. The pre-treatment and deposition processes described herein may also be performed in one continuous plasma process, or comprise two or more generated plasma, for example, one generated plasma for the pretreatment process and one or more generated plasma for the layer deposition steps; or one plasma for the pre-treatment process and initiation

layer deposition step and a second generated plasma for the dielectric deposition step.

[0039] The pre-treatment process comprises a plasma treatment of the underlying dielectric using an inert gas, an oxidizing gas, or both. The plasma treatment is believed to form a surface of the underlying dielectric material more similar to the subsequently deposited material. For example, it is believed the oxygen plasma creates a more oxide like surface. The plasma treatment may be performed in the same chamber used to deposit the silicon oxycarbide material.

[0040] One embodiment of the plasma treatment comprises providing an inert gas, including helium, argon, neon, xenon, krypton, or combinations thereof, to a processing chamber at a flow rate between about 500 sccm and about 3000 sccm, maintaining a chamber pressure between about 3 Torr and about 12 Torr, maintaining a substrate temperature between about 300°C and about 450°C, positioning a gas distributor, or "showerhead" that may be positioned between about 200 mils and about 1000 mils, for example between 300 mils and 500 mils from the substrate surface, and generating a plasma by applying a power density ranging between about 0.03 W/cm² and about 3.2 W/cm², which is a RF power level of between about 10 W and about 1000 W for a 200 mm substrate, at a high frequency such as between 13 MHz and 14 MHz, for example, 13.56MHz. The plasma treatment may be performed between about 3 seconds and about 120 seconds, for example, between about 5 seconds and about 40 seconds preferably used.

[0041] The plasma may be generated by a dual-frequency RF power source as described herein. Alternatively, all plasma generation may be performed remotely, with the generated radicals introduced into the processing chamber for plasma treatment of a deposited material or deposition of a material layer.

[0042] An example of the inert gas pre-treatment process comprises providing helium to a processing chamber at a flow rate of about 1500 sccm, maintaining a chamber pressure of about 5 Torr, maintaining a substrate temperature of about

350°C, positioning a gas distributor at about 450 mils from the substrate surface, and generating a plasma by applying a RF power level of about 300 W at a high frequency of about 13.56MHz for a period of 15 seconds.

[0043] The pre-treatment plasma process may utilize an oxidizing gas, such as oxygen, with or without the inert gas described above. The oxidizing pre-treatment process may comprise providing an oxidizing gas, such as oxygen or other oxidizing gas described herein, to a processing chamber at a flow rate between about 100 sccm and about 3000 sccm, maintaining a chamber pressure between about 2 Torr and about 12 Torr, maintaining a substrate temperature between about 250°C and about 450°C, positioning a gas distributor, or "showerhead" that may be positioned between about 200 mils and about 1000 mils, for example between about 300 mils and about 500 mils from the substrate surface, and generating a plasma by applying a power density ranging between about 0.03 W/cm² and about 3.2 W/cm², which is a RF power level of between about 10 W and about 1000 W for a 200 mm substrate, at a high frequency such as between 13 MHz and 14 MHz, for example, 13.56MHz. The plasma treatment may be performed between about 3 seconds and about 120 seconds, with a plasma treatment between about 5 seconds and about 40 seconds preferably used.

[0044] An example of the oxidizing gas pre-treatment process comprises providing oxygen to a processing chamber at a flow rate of about 750 sccm (about 1500 sccm for a dual station Producer™ plasma chamber), maintaining a chamber pressure of about 5 Torr, maintaining a substrate temperature of about 350°C, positioning a gas distributor at about 450 mils from the substrate surface, and generating a plasma by applying a RF power level of about 300 W at a high frequency of about 13.56MHz for a period of 15 seconds.

[0045] An initiation layer may be deposited on the underlying material, such as a silicon carbide layer that may comprise nitrogen or oxygen doped silicon carbide, to seed the deposition of subsequent dielectric layer, such as a silicon oxycarbide layer.

[0046] The initiation layer may comprise a silicon oxycarbide layer and may be deposited by an oxidizing gas and an organosilicon material, which organosilicon compound may be such compounds as described herein. The organosilicon compound and the oxidizing gas may be introduced into the processing chamber at a ratio of organosilicon compound (mgm) to oxidizing gas (sccm) of between about 1:2 and about 10:1, such as between about 1:2 and about 2:1, for example between about 1:2 and about 1:1. The initiation layer may be deposited at processing conditions approximately or equivalent to the subsequent dielectric material deposition, such as a silicon oxycarbide deposition.

[0047] The initiation layer and silicon oxycarbide layer may be deposited sequentially in situ by modifying the processing gas compositions. For example, the silicon oxycarbide layer may be deposited by introducing the organosilicon compound and the oxidizing gas into the processing chamber at a ratio of organosilicon compound (mgm) to oxidizing gas (sccm) of between about 10:1 or greater, such as between about 10:1 and about 20:1, for example about 18:1; and in situ processing may occur by changing the ratios of organosilicon compound and oxidizing gas between deposition of the initiation layer and silicon oxycarbide layer. The oxidizing gas may comprise an oxidizing compound selected from the group of oxygen, ozone, carbon monoxide, carbon dioxide, nitrous oxide, and combinations thereof, of which, oxygen is preferred.

[0048] The pre-treatment process may also be performed in situ with the initiation layer deposition and/or silicon oxycarbide layer deposition. All flow ratios between the organosilicon compound and the oxidizing gas for deposition process are described in units of mgm to sccm unless otherwise noted.

[0049] One embodiment of a deposition of dielectric initiation layer is as follows. The deposition may be performed by introducing an oxidizing compound at a flow rate between about 10 sccm and about 2000 sccm into the processing chamber, introducing an organosilicon precursor at a flow rate between about 100 milligrams/minute (mgm) and about 5000 mgm (which corresponds to between about 7 sccm and about 400 sccm for octamethylcyclotetrasiloxane (OMCTS)), and

optionally, supplying a noble gas at a flow rate between about 1 sccm and about 10000 sccm, maintaining the chamber at a substrate temperature between about 0°C and about 500°C, maintaining a chamber pressure between about 100 milliTorr and about 100 Torr, positioning a gas distributor between about 200 mils and about 700 mils from the substrate surface, and applying a RF power of between about 0.03 watts/cm² and about 1500 watts/cm², such as between about 0.03 W/cm² and about 6.4 W/cm², which is a RF power level of between about 10 W and about 2000 W for a 200 mm substrate.

[0050] The power may be applied from a dual-frequency RF power source a first RF power with a frequency in a range of about 10 MHz and about 30 MHz at a power in a range of about 200 watts to about 1000 watts and at least a second RF power with a frequency in a range of between about 100 KHz and about 500 KHz as well as a power in a range of about 1 watt to about 200 watts. The initiation layer may be deposited for a period of time between about 1 second and 60 seconds, for example between about 1 and about 5 seconds, such as 2 seconds.

[0051] An example of a deposition of initiation layer comprises introducing oxygen at a flow rate of 500 sccm into the processing chamber, introducing octamethylcyclotetrasiloxane at a flow rate of about 500 milligrams/minute (mgm) (which corresponds to about 39 sccm for OMCTS), introducing helium at a flow rate of about 4800 sccm, maintaining the chamber at a substrate temperature of about 350°C, maintaining a chamber pressure of about 5 Torr, positioning a gas distributor at about 350 mils from the substrate surface, and applying a RF power of about 500 watts at 13.56 MHz and about 150 watts at 356 KHz. The process was performed between about 1 and about 5 seconds, preferably about 2 seconds.

[0052] In an alternative embodiment of the initiation layer formation, an oxygen plasma pre-treatment process may be initiated and applied for a first period of time, and then the organosilicon material may be introduced for the initiation layer deposition. This allows for contiguous pre-treatment of the deposited material by an

oxidizing plasma and subsequent initiation layer deposition in situ prior to deposition of the subsequent dielectric material, which may also be performed in situ.

[0053] The dielectric material may comprise silicon oxycarbide deposited in one embodiment by introducing an oxidizing compound, such as oxygen, at a flow rate between about 10 sccm and about 2000 sccm into the processing chamber, introducing an organosilicon precursor at a flow rate between about 100 milligrams/minute (mgm) and about 5000 mgm (which corresponds to between about 7 sccm and about 400 sccm for OMCTS), and optionally, supplying a noble gas at a flow rate between about 1 sccm and about 10000 sccm, maintaining the chamber at a substrate temperature between about 0°C and about 500°C, maintaining a chamber pressure between about 100 milliTorr and about 100 Torr, positioning a gas distributor between about 200 mils and about 700 mils from the substrate surface, and applying a RF power of between about 0.03 watts/cm² and about 1500 watts/cm², such as between about 0.03 W/cm² and about 6.4 W/cm², which is a RF power level of between about 10 W and about 2000 W for a 200 mm substrate. The power may be applied from a dual-frequency RF power source a first RF power with a frequency in a range of about 10 MHz and about 30 MHz at a power in a range of about 200 watts to about 1000 watts and at least a second RF power with a frequency in a range of between about 100 KHz and about 500 KHz as well as a power in a range of about 1 watt to about 200 watts.

[0054] An example of a deposition of a dielectric layer comprises introducing oxygen at a flow rate of 160 sccm into the processing chamber, introducing octamethylcyclotetrasiloxane at a flow rate of about 2900 milligrams/minute (mgm) (which corresponds to about 226 sccm for OMCTS), introducing helium at a flow rate of about 1000 sccm, maintaining the chamber at a substrate temperature of about 350°C, maintaining a chamber pressure of about 5 Torr, positioning a gas distributor at about 450 mils from the substrate surface, and applying a RF power of about 500 watts at 13.56 MHz and about 150 watts at 356 KHz. The initiation layer deposition process and the dielectric layer may be deposited in situ and contiguous by adjusting the precursor flow rates and other processing parameters.

Examples

[0055] The following examples demonstrate various embodiments of the adhesion processes described herein as compared to a standard interlayer stack to illustrate the improved interlayer adhesion. The samples were undertaken using a Producer™ 300 mm processing chambers, which includes a solid-state dual frequency RF matching unit with a two-piece quartz process kit, both fabricated and sold by Applied Materials, Inc., Santa Clara, California.

[0056] Test samples were prepared as follows. A stack of dielectric layers were deposited on a silicon substrate as follows. The substrate comprises a silicon substrate having about 1000 Å of oxide disposed thereon, about 250 Å of tantalum disposed on the oxide, about 4500 Å of copper disposed on the tantalum, about 2000 Å of silicon carbonitride disposed on the copper layer, and about 2000 Å of silicon oxycarbide deposited on the silicon carbonitride layer. The silicon carbonitride deposition and the silicon oxycarbide deposition may be one continuous plasma or may comprise two or more generated plasmas.

[0057] The silicon oxycarbide layer was deposited by introducing oxygen at a flow rate of 160 sccm into the processing chamber, introducing octamethylcyclotetrasiloxane at a flow rate of about 2900 milligrams/minute (mgm) (which corresponds to about 226 sccm for OMCTS), introducing helium at a flow rate of about 1000 sccm, maintaining the chamber at a substrate temperature of about 350°C, maintaining a chamber pressure of about 5 Torr, positioning a gas distributor at about 450 mils from the substrate surface, and applying a RF power of about 500 watts at 13.56 MHz and about 150 watts at 356 KHz.

[0058] Adhesion testing was performed on the test samples as follows. Between about 120 µm and about 150 µm of epoxy material with known delamination characteristics were deposited on the test samples. A layer of silicon was deposited thereon. The test samples were then baked or cured for one hour at approximately 190°C and then cleaved into 1 cm by 1 cm samples and cooled to -170°C with liquid nitrogen. The samples were then observed to determine delamination, which occurs at a weakest interlayer interface at a given temperature. The shrinkage of

the epoxy at a given temperature correlates to the forces that are required to induce peeling. From this observation, a determination of adhesion can be calculated. Adhesion (G_C) is based on the formula $\sigma\sqrt{(h/2)}$, with h being the epoxy thickness and being the residual stress. The measured adhesion G_C of an untreated or unmodified stack described above was about 3 J-m^2 with a dielectric constant of about 3.01 and delamination at the silicon carbonitride and silicon oxycarbide interface.

[0059] In one sample, sample #1, a helium plasma treatment was performed on the silicon carbonitride layer prior to deposition of the silicon oxycarbide layer by providing helium to a processing chamber at a flow rate of about 1500 sccm, maintaining a chamber pressure of about 5 Torr, maintaining a substrate temperature of about 350°C , positioning a gas distributor at about 450 mils from the substrate surface, and generating a plasma by applying a RF power level of about 300 W at a high frequency of about 13.56MHz for a period of 15 seconds. The measured adhesion G_C of helium processed stack of sample #1 was about 3.8 J-m^2 with a dielectric constant of about 3.03 and delamination did not occur at the silicon carbonitride and silicon oxycarbide interface.

[0060] In another sample, sample #2, a helium plasma treatment and initiation layer was performed on the silicon carbonitride layer prior to deposition of the silicon oxycarbide layer by the helium process described in sample #1 and an initiation layer deposition by introducing oxygen at a flow rate of 500 sccm into the processing chamber, introducing octamethylcyclotetrasiloxane at a flow rate of about 500 milligrams/minute (mgm) (which corresponds to about 39 sccm for OMCTS), introducing helium at a flow rate of about 4800 sccm, maintaining the chamber at a substrate temperature of about 350°C , maintaining a chamber pressure of about 5 Torr, positioning a gas distributor at about 350 mils from the substrate surface, and applying a RF power of about 500 watts at 13.56 MHz and about 150 watts at 356 KHz. The measured adhesion G_C of helium processed stack of sample #2 was about 5.5 J-m^2 with a dielectric constant of about 3.06 and

delamination did not occur at the silicon carbonitride and silicon oxycarbide interface.

Layer Deposition:

Silicon Oxycarbide Layers

[0061] The silicon oxycarbide layer generally comprises silicon, carbon, and between about 15 atomic % or greater of oxygen. Oxygen doped silicon carbide as described herein comprise less than about 15 atomic % of oxygen. A preferred silicon oxycarbide layer comprises silicon-oxygen bonds and silicon-carbon bonds that contribute to low dielectric constants and barrier properties. The carbon content of the deposited layer is between about 5 atomic % and about 30 atomic % excluding hydrogen atoms, and is preferably between about 10 atomic % and about 20 atomic % excluding hydrogen atoms. The deposited layers may contain C-H or C-F bonds throughout to provide hydrophobic properties to the silicon oxycarbide layer. The silicon oxycarbide layer may also contain hydrogen, nitrogen, or combinations thereof.

[0062] The silicon oxycarbide layers are deposited by oxidizing organosilicon compounds, including both oxygen-containing organosilicon compounds and oxygen-containing organosilicon compounds, as described herein. In a preferred aspect of the invention, the silicon oxycarbide layer is deposited by reacting an organosilicon compound comprising three or more alkyl groups with an oxidizing gas comprising ozone. The silicon oxycarbide layer may be deposited without an oxidizer if the organosilicon compound includes oxygen. The preferred organosilicon compounds include, for example:

trimethylsilane,	$(\text{CH}_3)_3\text{-SiH}$
tetramethylsilane,	$(\text{CH}_3)_4\text{-Si}$
1,1,3,3-tetramethyldisiloxane,	$(\text{CH}_3)_2\text{-SiH-O-SiH-(CH}_3)_2$
hexamethyldisiloxane,	$(\text{CH}_3)_3\text{-Si-O-Si-(CH}_3)_3$
2,2-bis(1-methyldisiloxanyl)propane,	$(\text{CH}_3\text{-SiH}_2\text{-O-SiH}_2\text{)}_2\text{-C(CH}_3)_2$
1,3,5,7-tetramethylcyclotetrasiloxane,	$\text{-(-SiHCH}_3\text{-O-)}_4\text{- (cyclic)}$
octamethylcyclotetrasiloxane,	$\text{-(-Si(CH}_3)_2\text{-O-)}_4\text{- (cyclic)}$
1,3,5,7,9-pentamethylcyclopentasiloxane,	$\text{-(-SiHCH}_3\text{-O-)}_5\text{- (cyclic)}$

and fluorinated derivatives thereof.

[0063] The organosilicon compounds are oxidized during deposition of the silicon oxycarbide layer, preferably by reaction with oxygen (O_2), ozone (O_3), nitrous oxide (N_2O), carbon monoxide (CO), carbon dioxide (CO_2), water (H_2O), or combinations thereof, of which oxygen is preferred. When ozone is used as an oxidizing gas, an ozone generator typically converts about 15 wt.% of the oxygen in a source gas to ozone, with the remainder typically being oxygen. However, the ozone concentration may be increased or decreased based upon the amount of ozone desired and the type of ozone generating equipment used. Organosilicon compounds that contain oxygen may be disassociated to provide the oxygen. During deposition of the silicon oxycarbide layer, the substrate is maintained at a temperature between about $-20^\circ C$ and about $500^\circ C$, and preferably is maintained at a temperature between about $170^\circ C$ and about $180^\circ C$.

[0064] For a plasma enhanced deposition of the silicon oxycarbide layer, the organosilicon material is deposited using a power density ranging between about 0.003 W/cm^2 and about 6.4 W/cm^2 , which is a RF power level of between about 1 W and about 2000 W for a 200 mm substrate. Preferably, the RF power level is between about 300 W and about 1700 W. The RF power is provided at a frequency between about 0.01 MHz and 300 MHz. The RF power may be provided continuously or in short duration cycles wherein the power is on at the stated levels for cycles less than about 200 Hz, and the on cycles total between about 10% and about 50% of the total duty cycle. The deposition process of the low dielectric constant layer is performed in a substrate processing system described in more detail below. The silicon oxycarbide layer can be deposited continuously or with interruptions, such as changing chambers or providing cooling time, to improve porosity.

[0065] Alternatively, a dual-frequency system may be applied to deposit the silicon oxycarbide material. A dual-frequency source of mixed RF power provides a high frequency power in a range between about 10 MHz and about 30 MHz, for example, about 13.56 MHz, as well as a low frequency power in a range of between

about 100 KHz and about 500 KHz, for example, about 350 KHz. An example of a mixed frequency RF power application may include a first RF power with a frequency in a range of about 10 MHz and about 30 MHz at a power in a range of about 200 watts to about 1000 watts and at least a second RF power with a frequency in a range of between about 100 KHz and about 500 KHz as well as a power in a range of about 1 watt to about 200 watts. The ratio of the second RF power to the total mixed frequency power is preferably less than about 0.2 to 1.0.

[0066] In one aspect, a cyclic organosilicon compound and an aliphatic organosilicon compound are reacted with an oxidizing gas in amounts sufficient to deposit a low dielectric constant layer on a semiconductor substrate, wherein the cyclic organosilicon compound comprises at least one silicon-carbon bond. The aliphatic organosilicon compound contains a silicon-hydrogen bond or a silicon-oxygen bond, preferably a silicon-hydrogen bond. For example, the cyclic organosilicon compound may be 1,3,5,7-tetramethylcyclotetrasiloxane or octamethylcyclotetrasiloxane and the aliphatic organosilicon compound may be trimethylsilane or 1,1,3,3-tetramethyldisiloxane.

[0067] In another aspect, both the cyclic organosilicon compound and the aliphatic organosilicon compound contain a silicon-hydrogen bond. For example, 1,3,5,7-tetramethylcyclotetrasiloxane and trimethylsilane or 1,1,3,3-tetramethyldisiloxane are blended and oxidized while applying RF power.

[0068] In one embodiment of plasma enhanced deposition, oxygen or oxygen containing compounds are dissociated to increase reactivity and to achieve desired oxidation of the deposited layer. RF power is coupled to the deposition chamber to increase dissociation of the compounds. The compounds may also be dissociated in a microwave chamber prior to entering the deposition chamber.

[0069] Although deposition preferably occurs in a single deposition chamber, for example, the DxZ™ processing chamber or the Producer™ processing chamber, both of which are commercially available for Applied Materials, Inc., or Santa Clara, California, the silicon oxycarbide layer can be deposited sequentially in two or more

deposition chambers, e.g., to permit cooling of the layer during deposition. Additionally, the silicon oxycarbide and silicon carbide layers may be deposited in situ in the same chamber and deposited subsequently by using selective precursors and controlling the processing parameters and the composition of processing gases. For example, both the silicon carbide and silicon oxycarbide layer may be deposited using trimethylsilane with ammonia being used in the silicon carbide deposition to form a nitrogen doped silicon carbide, and subsequently using ozone during deposition of the silicon oxycarbide material.

Silicon Carbide Layers

[0070] The silicon carbide layer is deposited by reacting an organosilicon compound to form a dielectric layer comprising carbon-silicon bonds and a dielectric constant less than about 4. The silicon carbide layer is preferably an amorphous hydrogenated silicon carbide. The silicon carbide layer may be deposited in a plasma of an inert gas, hydrogen gas, or both. The silicon carbide dielectric layer may be a doped silicon carbide layer. The silicon carbide layer may be deposited as a barrier layer disposed adjacent a conductive material or dielectric layer or may be an etch stop deposited between one or more dielectric layers.

[0071] Examples of suitable organosilicon compounds used herein for silicon carbide deposition preferably include the structure:



wherein R includes organic functional groups including alkyl, alkenyl, cyclohexenyl, and aryl groups, in addition to functional derivatives thereof. The organic precursors may have more than one R group attached to the silicon atom, and the invention contemplates the use of organosilicon precursors with or without Si-H bonds.

[0072] The organosilicon compounds include aliphatic organosilicon compounds, cyclic organosilicon compounds, or combinations thereof, having at least one silicon-carbon bond, and optionally, the structure may include oxygen. Cyclic organosilicon compounds typically have a ring comprising three or more silicon

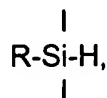
atoms. Aliphatic organosilicon compounds have linear or branched structures comprising one or more silicon atoms and one or more carbon atoms. Commercially available aliphatic organosilicon compounds include organosilanes that do not contain oxygen between silicon atoms, and for oxygen doped silicon carbide layer, organosiloxanes that contain oxygen between two or more silicon atoms. Fluorinated derivatives of the organosilicon compounds described herein may also be used to deposit the silicon carbide and silicon oxycarbide layers described herein.

[0073] Examples of suitable aliphatic and cyclic organosilicon compounds include, for example, one or more of the following compounds:

Methylsilane,	$\text{CH}_3\text{-SiH}_3$
Dimethylsilane,	$(\text{CH}_3)_2\text{-SiH}_2$
Trimethylsilane (TMS),	$(\text{CH}_3)_3\text{-SiH}$
Ethylsilane,	$\text{CH}_3\text{-CH}_2\text{-SiH}_3$
Disilanomethane,	$\text{SiH}_3\text{-CH}_2\text{-SiH}_3$
Bis(methylsilano)methane,	$\text{CH}_3\text{-SiH}_2\text{-CH}_2\text{-SiH}_2\text{-CH}_3$
1,2-disilanoethane,	$\text{SiH}_3\text{-CH}_2\text{-CH}_2\text{-SiH}_3$
1,2-bis(methylsilano)ethane,	$\text{CH}_3\text{-SiH}_2\text{-CH}_2\text{-CH}_2\text{-SiH}_2\text{-CH}_3$
2,2-disilanopropane,	$\text{SiH}_3\text{-C}(\text{CH}_3)_2\text{-SiH}_3$
1,3,5-trisilano-2,4,6-trimethylene,	$\text{-}(\text{-SiH}_2\text{CH}_2\text{-})_3\text{- (cyclic).}$

The above list is illustrative and should not be construed or interpreted as limiting the scope of the invention.

[0074] Phenyl containing organosilicon compounds may also be used for depositing the silicon carbide materials and generally include the structure:



wherein R is a phenyl group. For example, suitable phenyl containing organosilicon compounds generally includes the formula $\text{SiH}_a(\text{CH}_3)_b(\text{C}_6\text{H}_5)_c$, wherein a is 0 to 3, b is 0 to 3, and c is 1 to 4, and $a+b+c$ is equal to 4. Examples of suitable precursors derived from this formula include diphenylsilane, dimethylphenylsilane,

diphenylmethylsilane, phenylmethylsilane, and combinations thereof. Preferably used are phenyl containing organosilicon compounds with b is 1 to 3 and c is 1 to 3. The most preferred organosilicon compounds for deposition as barrier layer materials include organosilicon compounds having the formula $\text{SiH}_a(\text{CH}_3)_b(\text{C}_6\text{H}_5)_c$, wherein a is 1 or 2, b is 1 or 2, and c is 1 or 2. Examples of preferred precursors include dimethylphenylsilane and diphenylmethylsilane.

[0075] Generally, the organosilicon compounds are reacted in a plasma comprising a relatively inert gas, such as nitrogen (N_2) and noble gases, such as helium or argon. The deposited silicon carbide layers have dielectric constants of about 5 or less, and the doped silicon carbide layers may have dielectric constants of about 3 or less.

[0076] A preferred silicon carbide layer is deposited in one embodiment by supplying trimethylsilane to a plasma processing chamber at a flow rate between about 10 milligrams/min (mgm) and about 5000 milligrams/min (mgm). Since conversion of milligram/minutes to standard cubic centimeters per minute (sccm) may vary between organosilicon compounds, milligrams/min is preferably used for organosilicon compounds. An inert gas, such as helium, argon, or combinations thereof, is also supplied to the chamber at a flow rate between about 50 sccm and about 5000 sccm. The chamber pressure is maintained between about 100 milliTor and about 15 Torr. The substrate surface temperature is maintained between about 100°C and about 450°C during the deposition process. An example process for depositing a silicon carbide layer is disclosed in U.S. Patent Serial No. 6,537,733, issued on March 25, 2003, which is incorporated by reference to the extent not inconsistent with the claims and disclosure described herein.

[0077] The silicon carbide layer may also be a doped silicon carbide layer containing oxygen, nitrogen, boron, phosphorus, or combinations thereof. Doped silicon carbide generally includes less than about 15 atomic percent (atomic %) or less of one or more dopants. Dopants may be used in the processing gases at a

ratio of dopant to organosilicon compound between about 1:5 or greater, such as between about 1:5 and about 1:100.

[0078] An oxygen source or a nitrogen source may be used during the reaction to form the oxygen doped and/or nitrogen doped silicon carbide layers. Examples of oxygen sources include oxidizing gases, such as oxygen, ozone, carbon monoxide, carbon dioxide, nitrous oxide, and oxygen containing organosilicon precursor, or combinations thereof, such as carbon monoxide and an oxygen containing organosilicon precursor. Oxygen doped silicon carbide generally includes less than about 15 atomic percent (atomic %) of oxygen, preferably about 10 atomic % or less of oxygen.

[0079] The oxygen containing organosilicon compounds include, for example:

Dimethyldimethoxysilane,	$(\text{CH}_3)_2\text{-Si-(OCH}_3)_2$
1,3-dimethyldisiloxane,	$\text{CH}_3\text{-SiH}_2\text{-O-SiH}_2\text{-CH}_3$
1,1,3,3-tetramethyldisiloxane (TMDSO),	$(\text{CH}_3)_2\text{-SiH-O-SiH-(CH}_3)_2$
Hexamethyldisiloxane (HMDS),	$(\text{CH}_3)_3\text{-Si-O-Si-(CH}_3)_3$
1,3-bis(silanomethylene)disiloxane,	$(\text{SiH}_3\text{-CH}_2\text{-SiH}_2\text{)}_2\text{-O}$
Bis(1-methyldisiloxanyl)methane,	$(\text{CH}_3\text{-SiH}_2\text{-O-SiH}_2\text{)}_2\text{-CH}_2$
2,2-bis(1-methyldisiloxanyl)propane,	$(\text{CH}_3\text{-SiH}_2\text{-O-SiH}_2\text{)}_2\text{-C(CH}_3)_2$
2,4,6,8-tetramethylcyclotetrasiloxane (TMCTS)	$\text{-(-SiHCH}_3\text{-O-)}_4\text{- (cyclic)}$
octamethylcyclotetrasiloxane (OMCTS),	$\text{-(-Si(CH}_3)_2\text{-O-)}_4\text{- (cyclic)}$
2,4,6,8,10-pentamethylcyclopentasiloxane,	$\text{-(-SiHCH}_3\text{-O-)}_5\text{- (cyclic)}$
1,3,5,7-tetrasilano-2,6-dioxy-4,8-dimethylene,	$\text{-(-SiH}_2\text{-CH}_2\text{-SiH}_2\text{-O-)}_2\text{- (cyclic)}$
Hexamethylcyclotrisiloxane	$\text{-(-Si(CH}_3)_2\text{-O-)}_3\text{- (cyclic)}$
1,3-dimethyldisiloxane,	$\text{CH}_3\text{-SiH}_2\text{-O-SiH}_2\text{-CH}_3$
hexamethoxydisiloxane (HMDOS)	$(\text{CH}_3\text{O})_3\text{-Si-O-Si-(OCH}_3)_3$

and fluorinated derivatives thereof.

[0080] Nitrogen doped silicon carbide may comprise up to 20 atomic % of nitrogen and may be deposited by the addition of nitrogen containing compounds including, for example, ammonia, nitrogen gas, a mixture of nitrogen and hydrogen gas, and compounds having Si-N-Si bonding groups, such as silazane compounds. Examples of suitable silazane precursors include aliphatic compounds, such as

hexamethyldisilazane and divinyltetramethyldisilazane, as well as cyclic compounds, such as hexamethylcyclotrisilazane.

[0081] For example, a doped silicon carbide layer can be deposited by introducing an oxygen source and/or a nitrogen source, or other dopant, into the processing chamber at a flow rate between about 50 sccm and about 10,000 sccm. For example, a nitrogen containing or nitrogen doped silicon carbide layer may be deposited by introducing a nitrogen source, such as ammonia, nitrogen, a mixture of nitrogen and hydrogen, or combinations thereof, during deposition of the silicon carbide layer.

[0082] Phosphorus and/or boron doping of the low k silicon carbide layer may be performed by introducing phosphine (PH_3) or borane (BH_3), or borane derivative thereof, such as diborane (B_2H_6), into the chamber during the deposition process. It is believed that dopants may reduce the dielectric constant of the deposited silicon carbide material. Phosphorus and/or boron dopants may be introduced into the processing chamber at a flow rate between about 50 sccm and about 10,000 sccm.

[0083] Organic compounds, such as aliphatic hydrocarbon compounds may also be used in the processing gas to increase the carbon content of the deposited silicon carbide materials. Suitable aliphatic hydrocarbon compounds include compounds having between one and about 20 adjacent carbon atoms. The hydrocarbon compounds can include adjacent carbon atoms that are bonded by any combination of single, double, and triple bonds.

[0084] Example processes for depositing a nitrogen containing silicon carbide layer is disclosed in U.S. Patent Application Serial No. 09/627,667, filed on July 28, 2000 February 23, 2001, and U.S. Patent Serial No. 6,537,733, issued on March 25, 2003, which are incorporated by reference to the extent not inconsistent with the claims and disclosure described herein. An example process for depositing an oxygen containing silicon carbide layer is disclosed in U.S. Patent Application Serial No. 10/196,498, filed on July 15, 2002, which is incorporated by reference to the extent not inconsistent with the claims and disclosure described herein. An example

process for depositing a boron and/or phosphorus silicon carbide layer is disclosed in U.S. Patent Application Serial No. 10/342,079, filed on January 13, 2003, which is incorporated by reference to the extent not inconsistent with the claims and disclosure described herein.

[0085] Generally, the organosilicon compound, inert gas, and optional dopant, are introduced to the processing chamber via a gas distribution plate spaced between about 200 millimeters (mm) and about 600 millimeters from the substrate on which the silicon carbide layer is being deposited. Power may be applied for a single or dual frequency RF power source. For example, power from a single 13.56 MHz RF power source is supplied to the chamber 10 to form the plasma at a power density between about 0.003 watts/cm² and about 3.2 watts/cm², or a power level between about 1 watt and about 1000 watts for a 200 mm substrate. A power density between about 0.9 watts/cm² and about 2.3 watts/cm², or a power level between about 300 watts and about 700 watts for a 200 mm substrate, is preferably supplied to the processing chamber to generate the plasma.

[0086] Alternatively, a dual-frequency system may be applied to deposit the silicon carbide material. A dual-frequency source of mixed RF power provides a high frequency power in a range between about 10 MHz and about 30 MHz, for example, about 13.56 MHz, as well as a low frequency power in a range of between about 100 KHz and about 500 KHz, for example, about 350 KHz. An example of a mixed frequency RF power application may include a first RF power with a frequency in a range of about 10 MHz and about 30 MHz at a power in a range of about 200 watts to about 1000 watts and at least a second RF power with a frequency in a range of between about 100 KHz and about 500 KHz as well as a power in a range of about 1 watt to about 200 watts. The ratio of the second RF power to the total mixed frequency power is preferably less than about 0.2 to 1.0.

[0087] Additionally, the ratio of the silicon source to the dopant in the gas mixture should have a range between about 1:1 and about 100:1. The above process parameters provide a deposition rate for the silicon carbide layer in a range between about 100 Å/min and about 3000 Å/min when implemented on a 200 mm

(millimeter) substrate in a deposition chamber available from Applied Materials, Inc., located in Santa Clara, California.

[0088] The embodiments described herein for depositing silicon carbide layers are provided to illustrate the invention, the particular embodiment shown should not be used to limit the scope of the invention. The invention also contemplates other processes and materials used to deposit silicon carbide layers.

[0089] While the foregoing is directed to preferred embodiments of the present invention, other and further embodiments of the invention may be devised without departing from the basic scope thereof, and the scope thereof is determined by the claims which follow.